

Embedded System Design

Sandeepani is the training division of CoreEL Technologies (I) Pvt Ltd and Authorized Training Provider for Xilinx in India for past 20 years

Course Description:

This course describes the building of an embedded system on Xilinx 7-series FPGA. It explores the soft core and hard core processor from Xilinx, and the use of Vitis to design the system and build an application program that runs on the system.

This course combines lectures with lab exercises to reinforce the concepts.

Who can attend?

- Engineers who want to understand building embedded system using Xilinx soft core and hard core processor utilizing the FPGA resources

Pre-requisites:

- Basic HDL knowledge (Verilog/VHDL)
- Understanding of C
- Knowledge of Xilinx 7-series FPGA Architecture

Software Tools:

- Vivado Design Suite System Edition
- Vitis IDE

Hardware:

- Architecture: Zynq UltraScale+ MPSoC (Cortex-A53 and Cortex-R5 processor)
- Demo board: Zynq UltraScale+ MPSoC ZCU104 board

Course Duration:

- 3 days (9 Hours – 3 hours per day)

What do I gain?

- Understand soft core and hard core processor IPs and their roles in Xilinx
- FPGA based embedded systems
- Explore architecture of Xilinx Microblaze soft core IP
- Introduction to Embedded System Software design that helps building applications

Registration link: [Click here to register](#)